

CPU AND POWER POD VALID CONFIGURATIONS

NUMBER OF INSTALLED CPUs							
1	2	3	4	5	6	7	8

NOTES:

Unloaded CPU or POD.

These symbols indicate loaded POD(s). NOTE: P0 and P2 are closest to the front and get the coolest airflow.

These symbols indicate loaded CPU(s), with color coordinated with the PODs.

1) PDC insures CPUs, CPU revision, CPU bin, POD type, and system frequency are configured correctly.

2) PDC will block boot beyond bch, if the CPUs are not loaded with one of the above valid configurations.

3) 3, 5, 6 & 7 way CPU configurations are NOT required for limited shipment release or initial volume shipments release per PCR-018.

These two non-POR 3 & 4 WAY configurations may perform better in cache intensive applications. In the event of a strong business need, a PCR, additional engineering & a possible airflow baffle will be needed.

MAIN MEMORY VALID CONFIGURATIONS

SLOT NUMBER OF INSTALLED MEMORY CARRIERS										NUMBER OF DIMM PAIRS
0 "Z J40"	1 "X J39"	0 "Z J40"	2 "W J42"	1 "X J39"	0 "Z J40"	3 "Y J41"	2 "W J42"	1 "X J39"	0 "Z J40"	
										1
										2
										3
										4
										5
										6
										7
										8
										9
										10
										11
										12
										13
										14
										15
										16

NOTES:

DIMM pair not installed.

DIMM pair installed in slot 0a & 0b.

DIMM pair installed in slot 1a & 1b.

DIMM pair installed in slot 2a & 2b.

DIMM pair installed in slot 3a & 3b.

DIMM pair may have to be moved when adding carrier(s) & next DIMM pair.

DIMM pair will be relocated when adding carrier(s) & next DIMM pair.

1) PDC deallocates DIMM pairs, if the DIMMs are not the same type.

2) PDC will give a warning, if the DIMM pairs and memory carriers are not loaded with one of the above valid configurations.

3) For best performance, load DIMM types together using the 4 memory carrier configurations.

4) 2 & 3 memory carrier configurations are NOT required for limited shipment release or initial volume shipments release per PCR-018.